



EMULATION TECHNOLOGY, INC.

Specification Number: M2043

PROGRAMMING ADAPTER SPECIFICATION

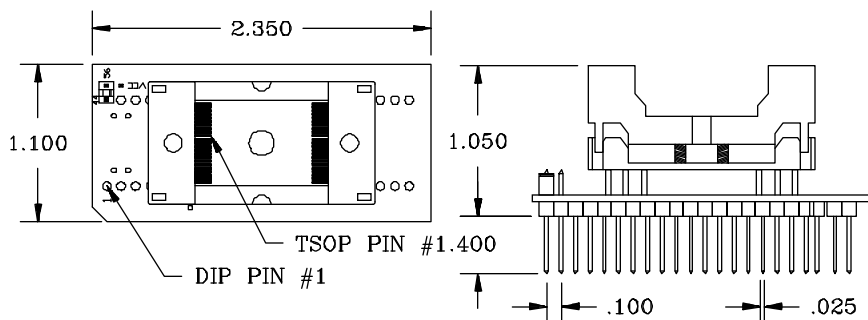
Item: 564402TS600YAMS

Description: AS-56-44-02TS-6YAM-GANG-S (Rev. B)

REPLACEMENT SOCKET: S-TSO-00-056-A

Call Emulation Technology for a replacement socket price and delivery quote.

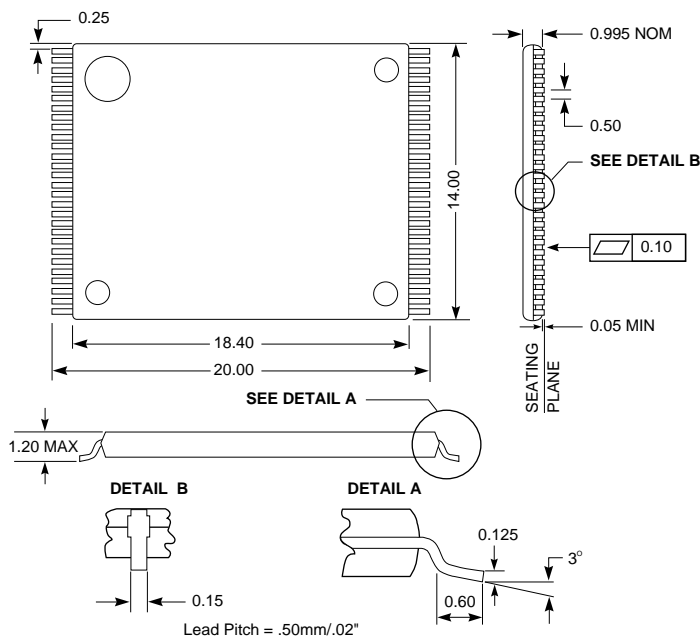
TYPICAL DIMENSIONS:



All Dimensions in Inches.

(Assembly Drawing: F2043)

PACKAGE SPECIFICATIONS:



All Dimensions in Millimeters.

ET Code: 056-TS01

Compatible TSOP devices:

E28F016SA
E28F016SV

Programmers:

B&C Microsystems: Proteus(UPLC88/72/68/48)
Data I/O: ChipLab-48, 2900
Emulation Technology, Inc.: ET-PIC 4000/48

Manufacturer:

Intel

CONNECTION TABLE:

56-Lead TSOP SOCKET	PIN Names	44-Pin DIP Base
1	3/5#	GND Plane
2	CE1# ¹	10
3	NC	NO TRACE
4	A20	43
5	A19	1
6	A18	2
7	A17	3
8	A16	4
9	V _{CC}	42
10	A15	5
11	A14	6
12	A13	7
13	A12	8
14	CE0# ¹	10
15	V _{PP}	9
16	RP#	V _{CC} Plane
17	A11	11
18	A10	12
19	A9	13
20	A8	14
21	GND	22 & 35
22	A7	15
23	A6	16
24	A5	17
25	A4	18
26	A3	19
27	A2	20
28	A1	21
29	NC	NO TRACE
30	NC	NO TRACE
31	BYTE#	V _{CC} Plane
32	A0	NO TRACE
33	DQ0	23
34	DQ8	24
35	DQ1	25
36	DQ9	26
37	V _{CC}	42
38	DQ2	27
39	DQ10	28
40	DQ3	29
41	DQ11	30
42	GND	22 & 35
43	V _{CC}	42
44	DQ4	31
45	DQ12	32
46	DQ5	33
47	DQ13	34
48	GND	22 & 35
49	DQ6	36
50	DQ14	37
51	DQ7	38
52	DQ15	39
53	RY/BY#	NO TRACE
54	OE#	40
55	WE#	41
56	WP# ²	44/V _{CC} Plane

Note:

- TSOP pins 2 (CE1#) & 14 (CE0#) are tied together on the interposer board.
- TSOP pin 56 (WP#) ties to a center jumper post; it can be jumpered to either a post tied to DIP pin 44 or a post connected to the interposer board V_{CC} plane.



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